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## 52<del>9:41089X007NT0564</del>U\$

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s):

OKUDA, et al.

Serial No.:

10/050,519

Filed:

January 18, 2002

For:

METHOD AND SYSTEM FOR INSPECTING ELECTRONIC

CIRCUIT PATTERN

Group:

2623

Examiner:

V. Bali

Conf. No.:

7762

## **AMENDMENT**

Commissioner for Patents P.O. Box 1450 Alexandria, Virginia 22313-1450

May 12, 2006

Sir:

In response to the Office Action dated October 13, 2005, and further to the filing of a Notice of Appeal on March 13, 2006, please amend the above-identified application as listed below and as set forth on the following pages:

## **Amendments to the Claims**

Remarks are included following the amendments